

File 1

FIG. 2 is a block diagram of a system 140 in accordance with the present invention. The system 140 includes a host 211, a look-up block 220, a SRAM 220, a SPRE 230, and a physical layer 250. The host 211 is connected to the look-up block 220, which is connected to the SRAM 220. The SRAM 220 is connected to the SPRE 230, which is connected to the physical layer 250. The physical layer 250 is connected to a network 141. The system 140 is also connected to a network 146.

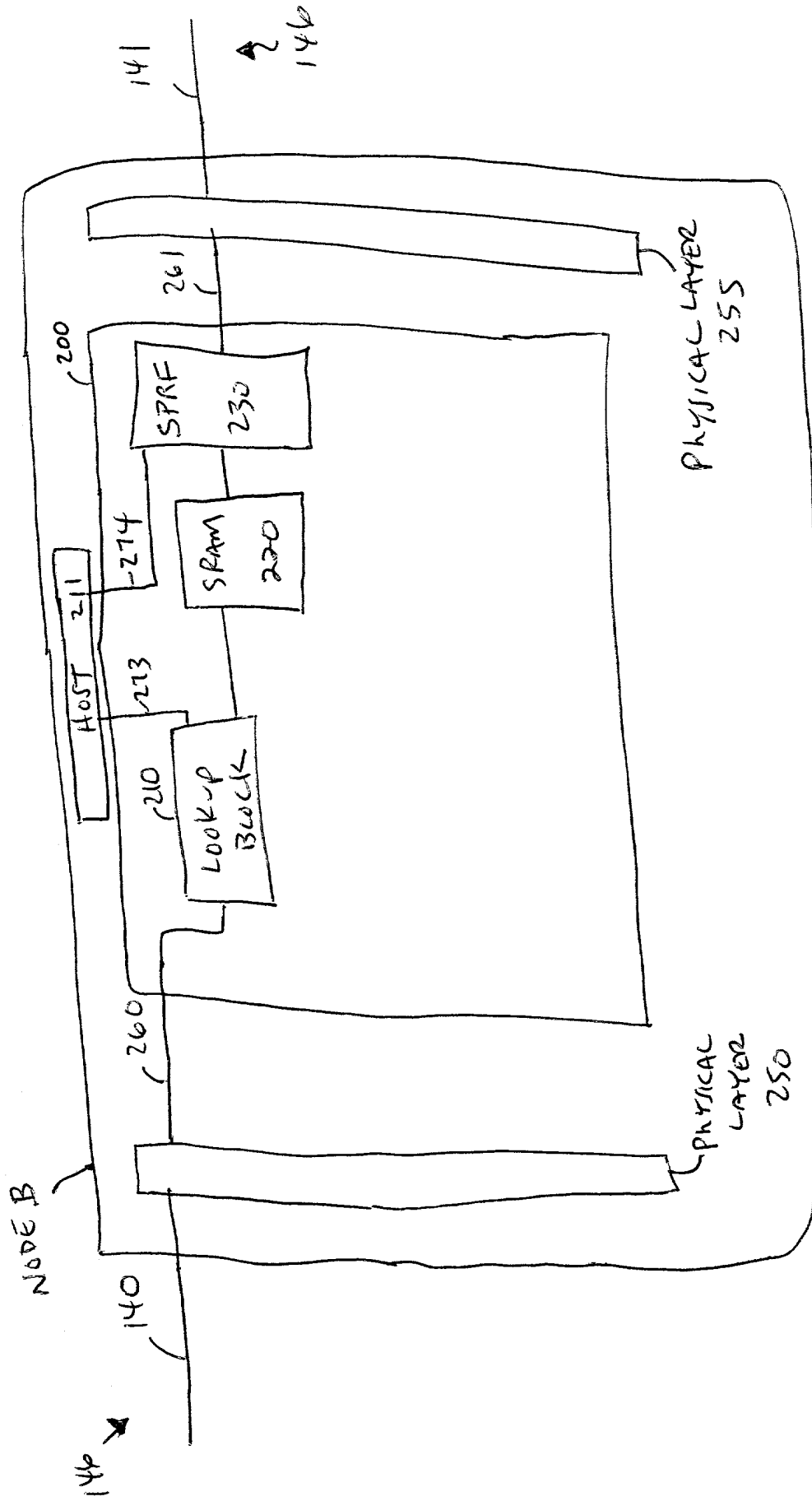


FIG. 2

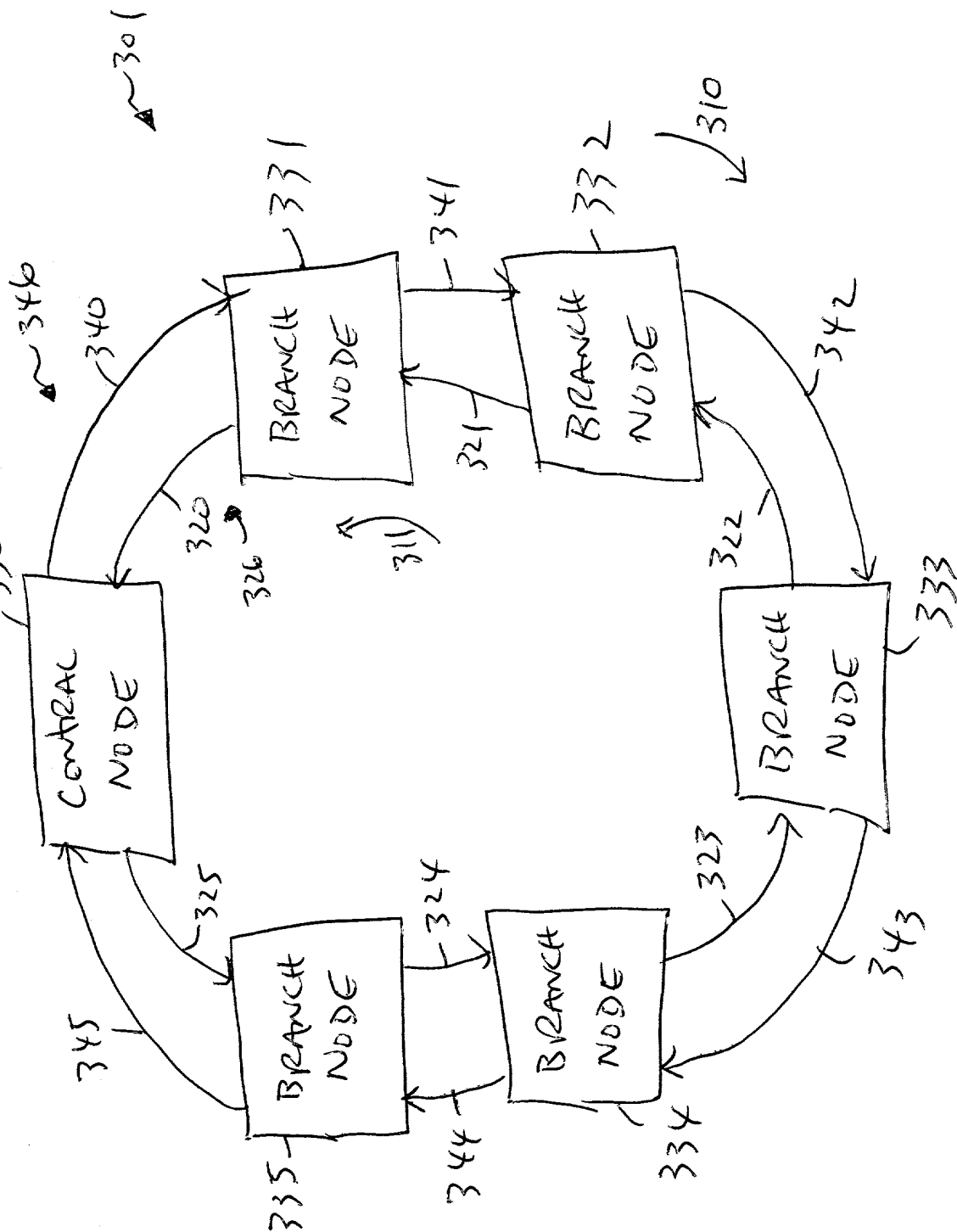
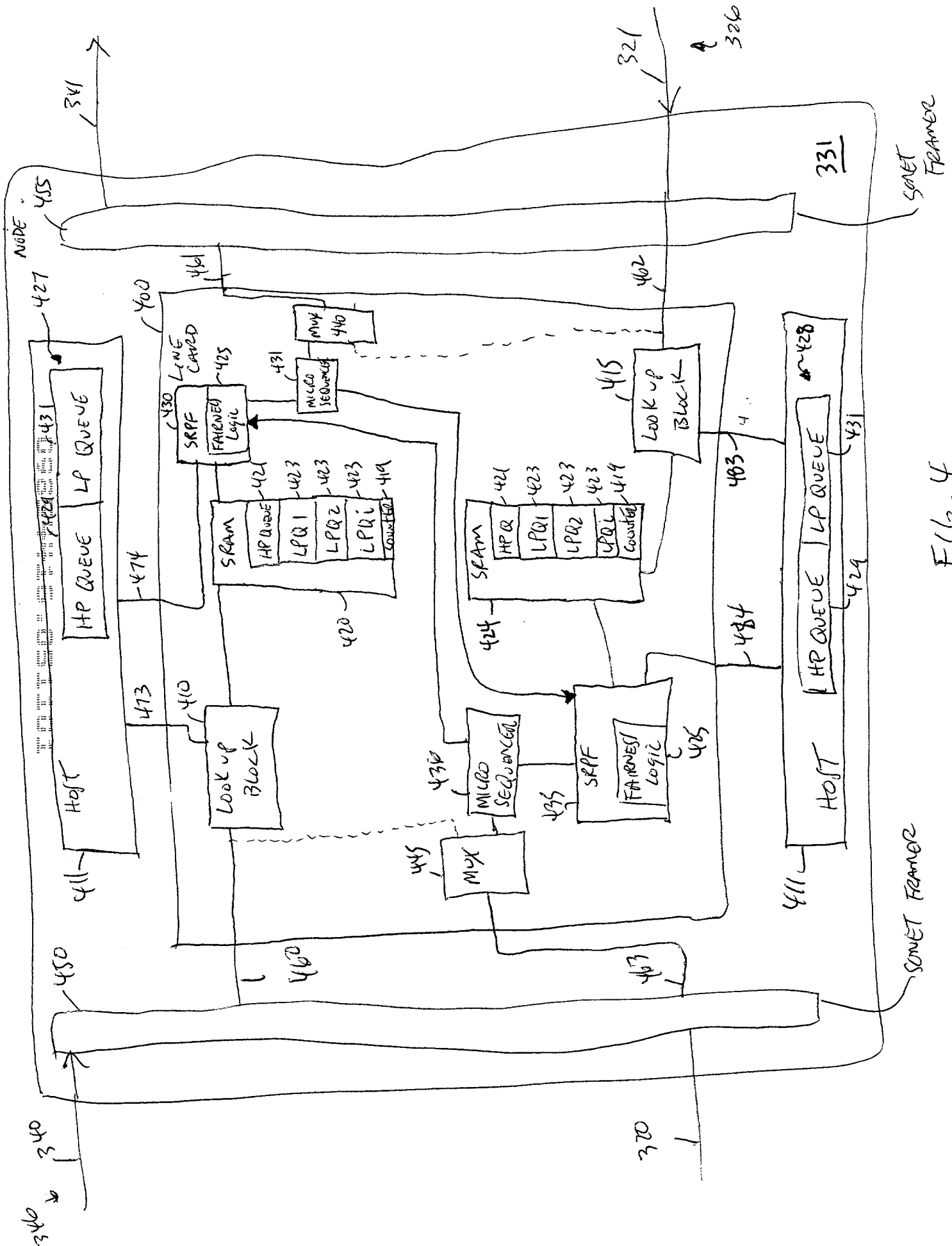


FIG. 3



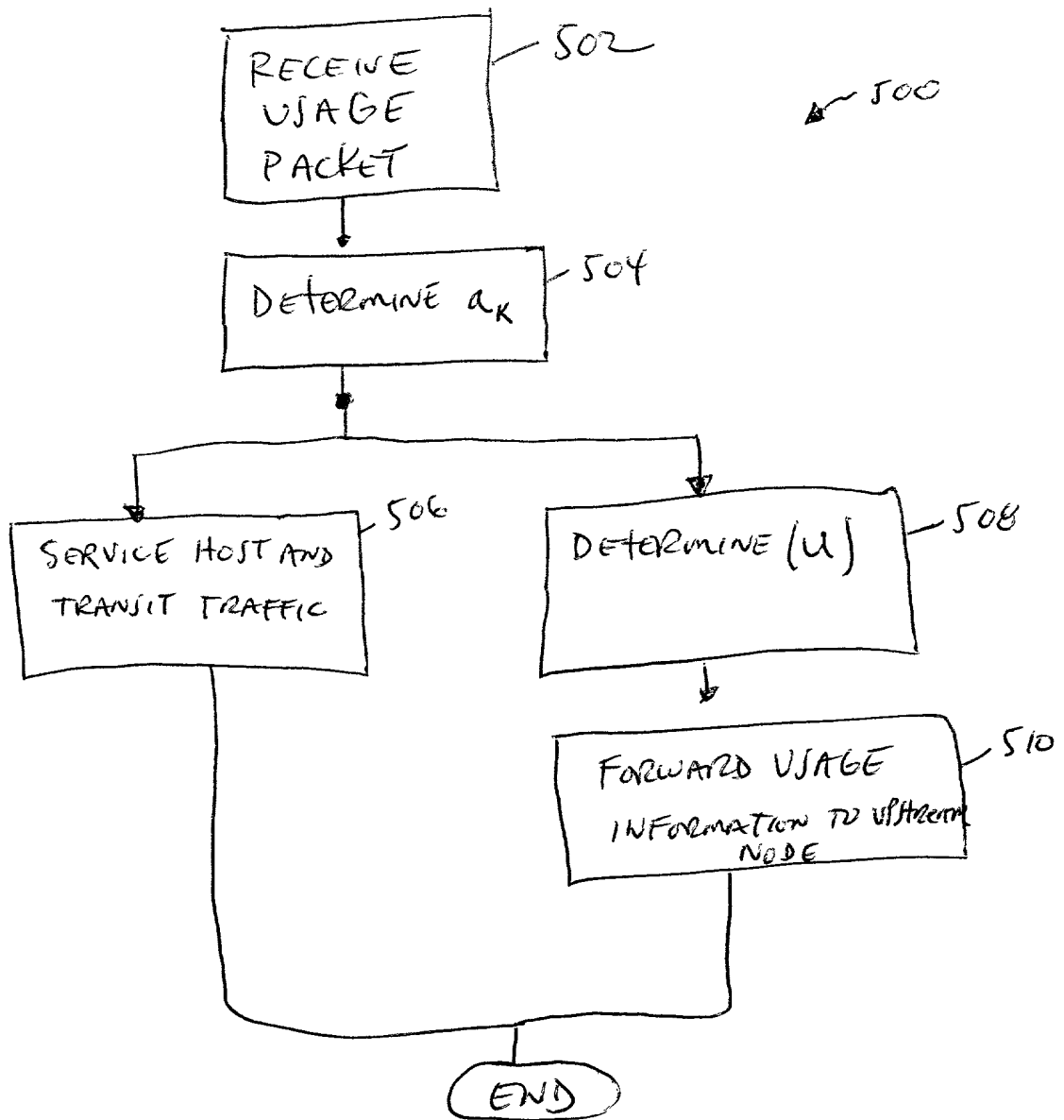


FIGURE 5a

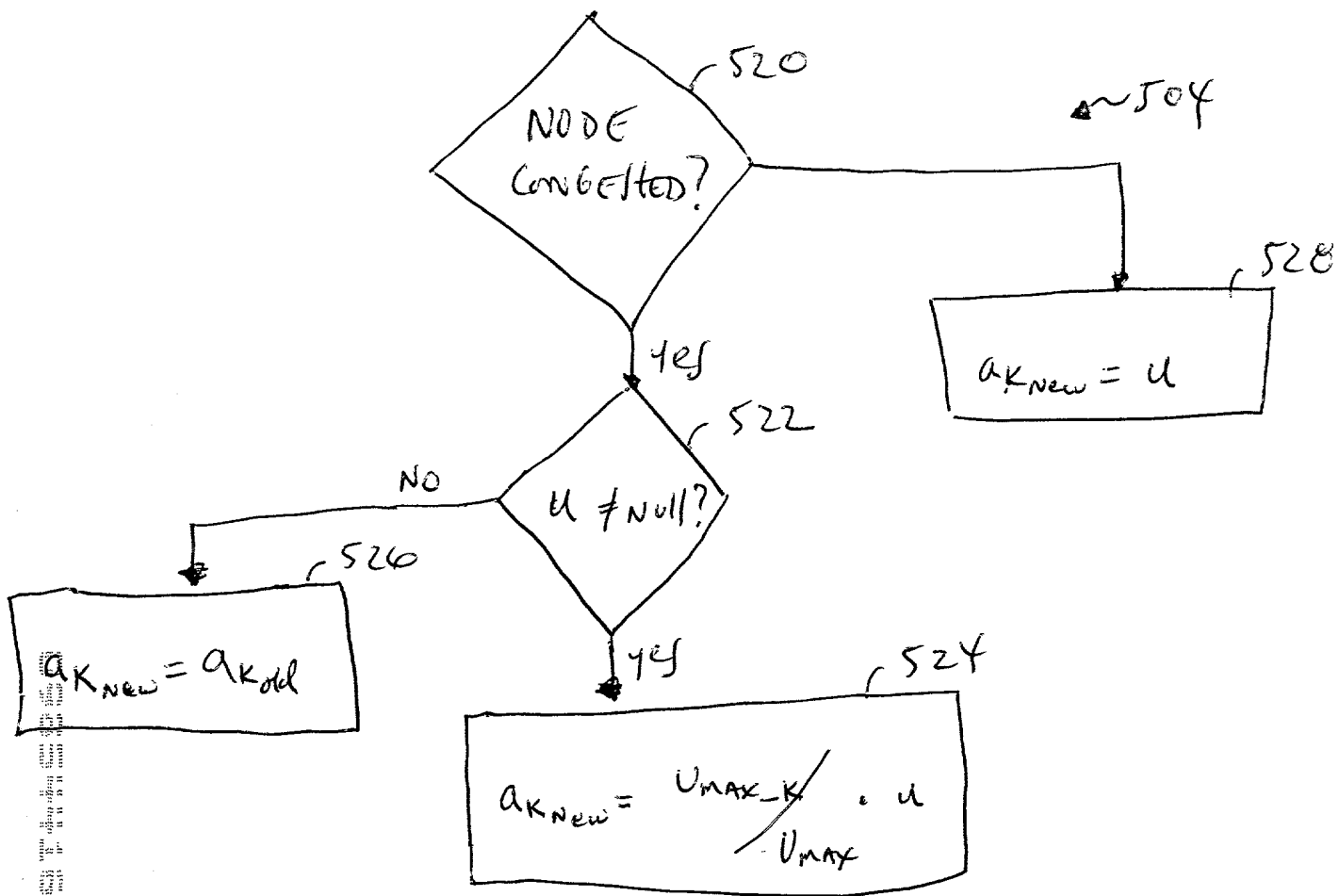
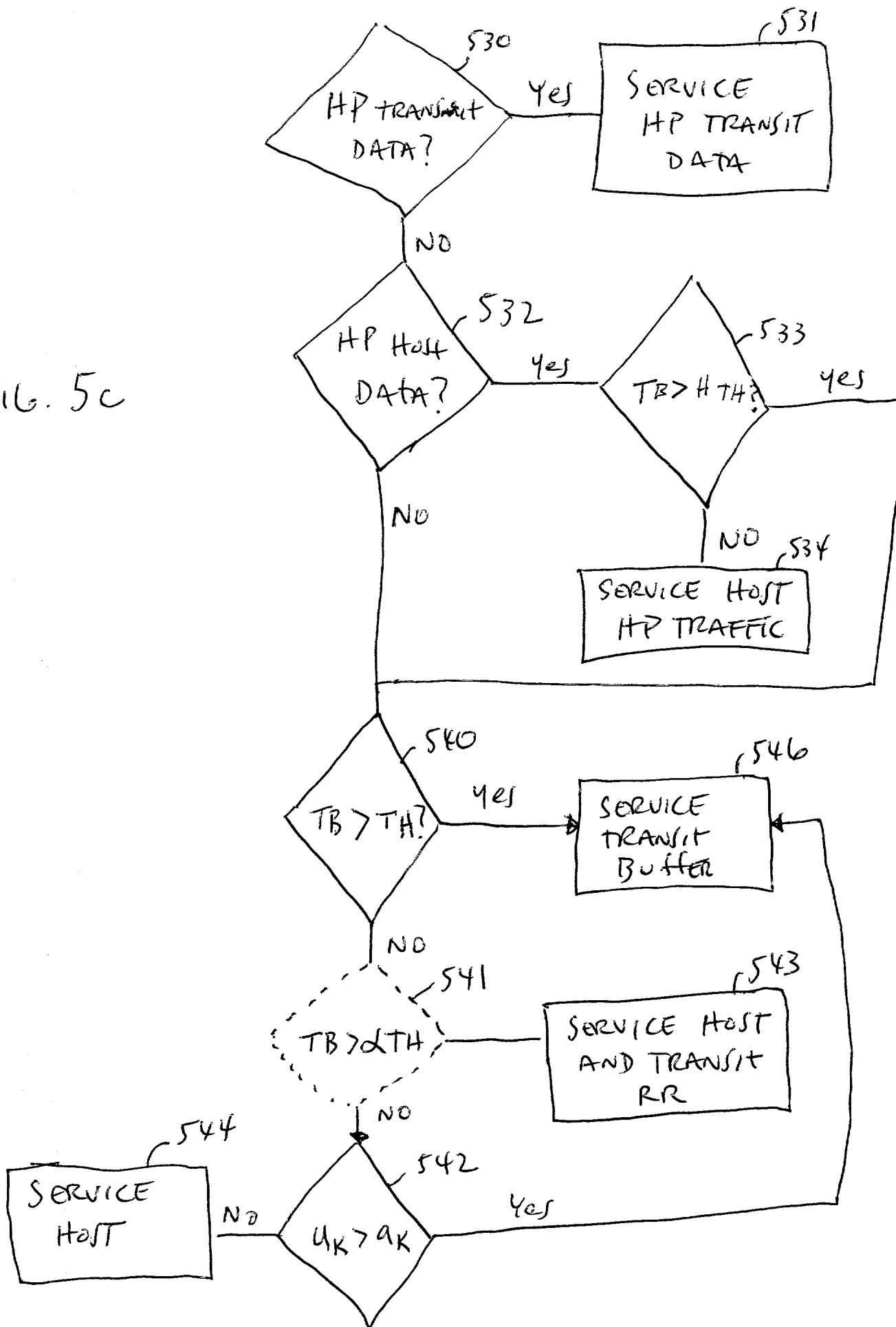


FIG. 5b

FIG. 5c



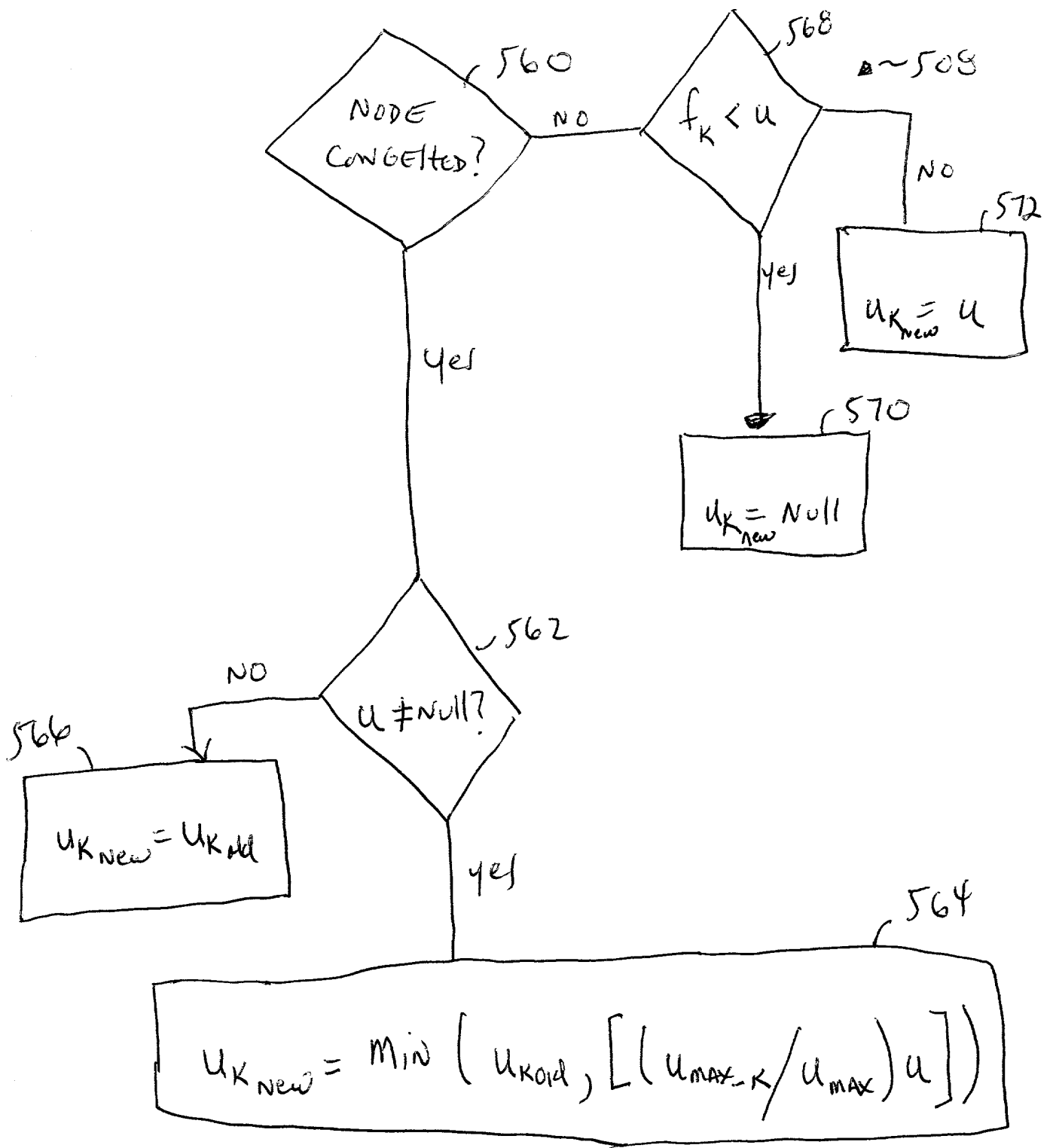


FIG. 5d



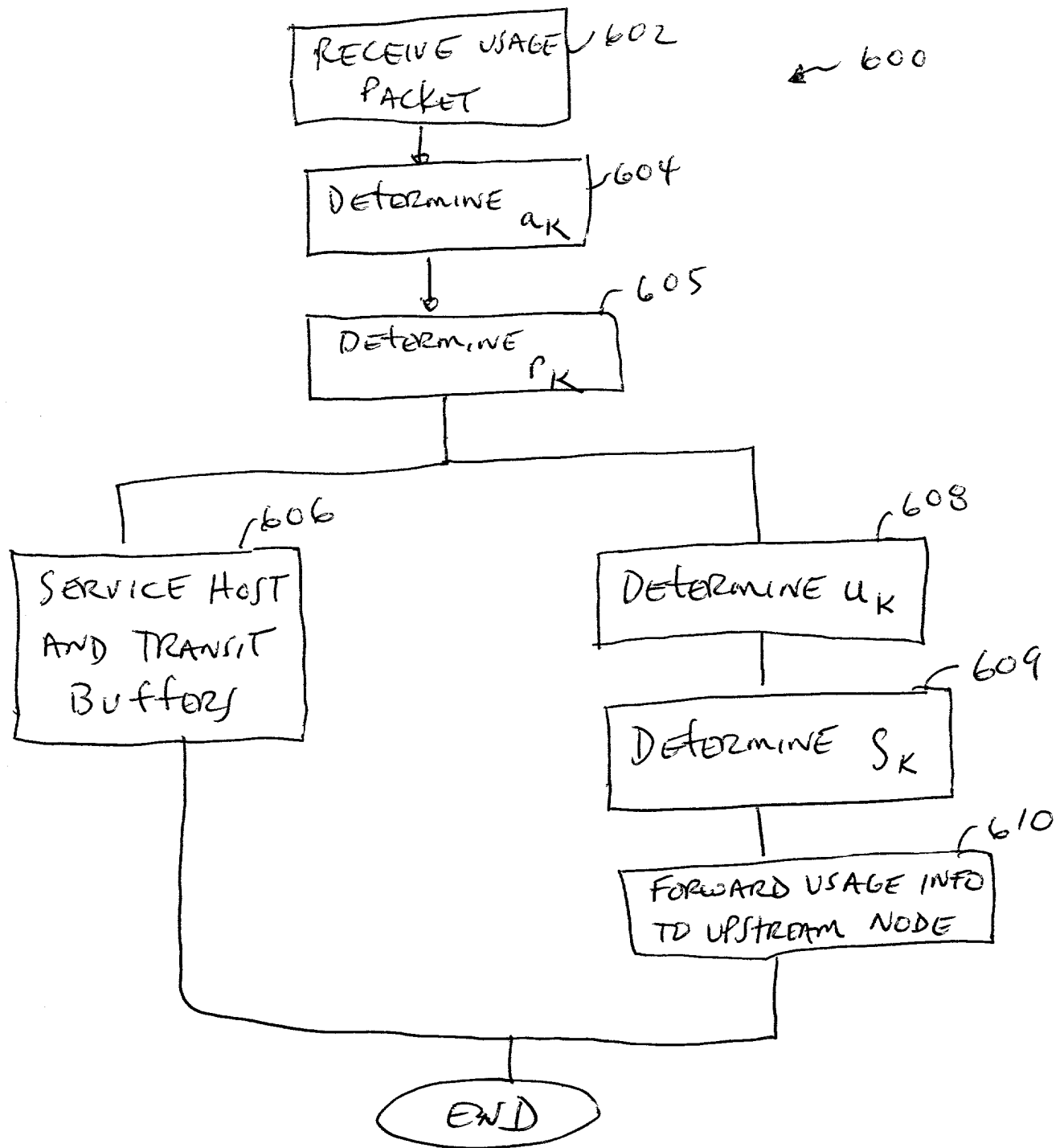


FIG. 6a

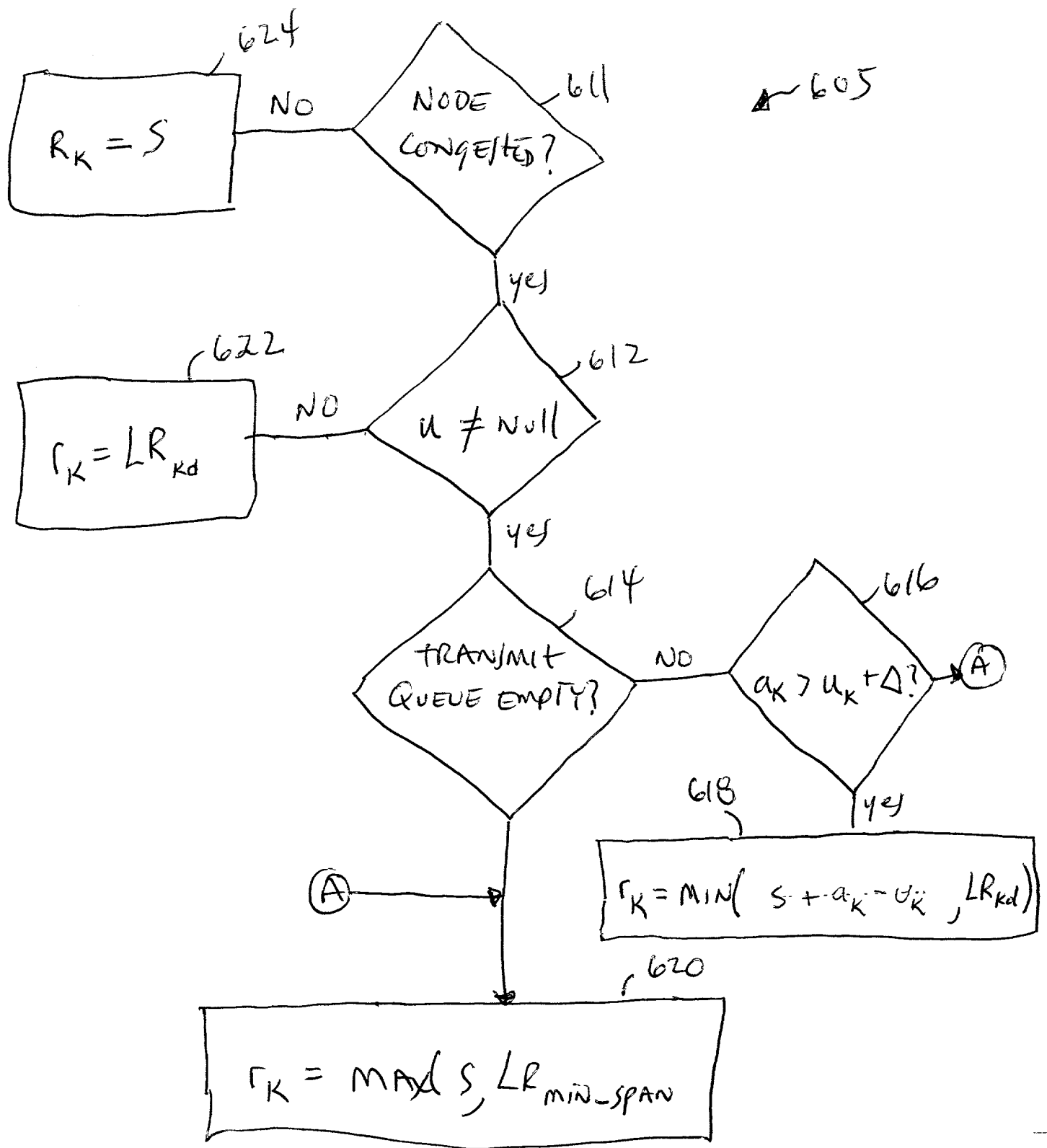


FIG. 6B

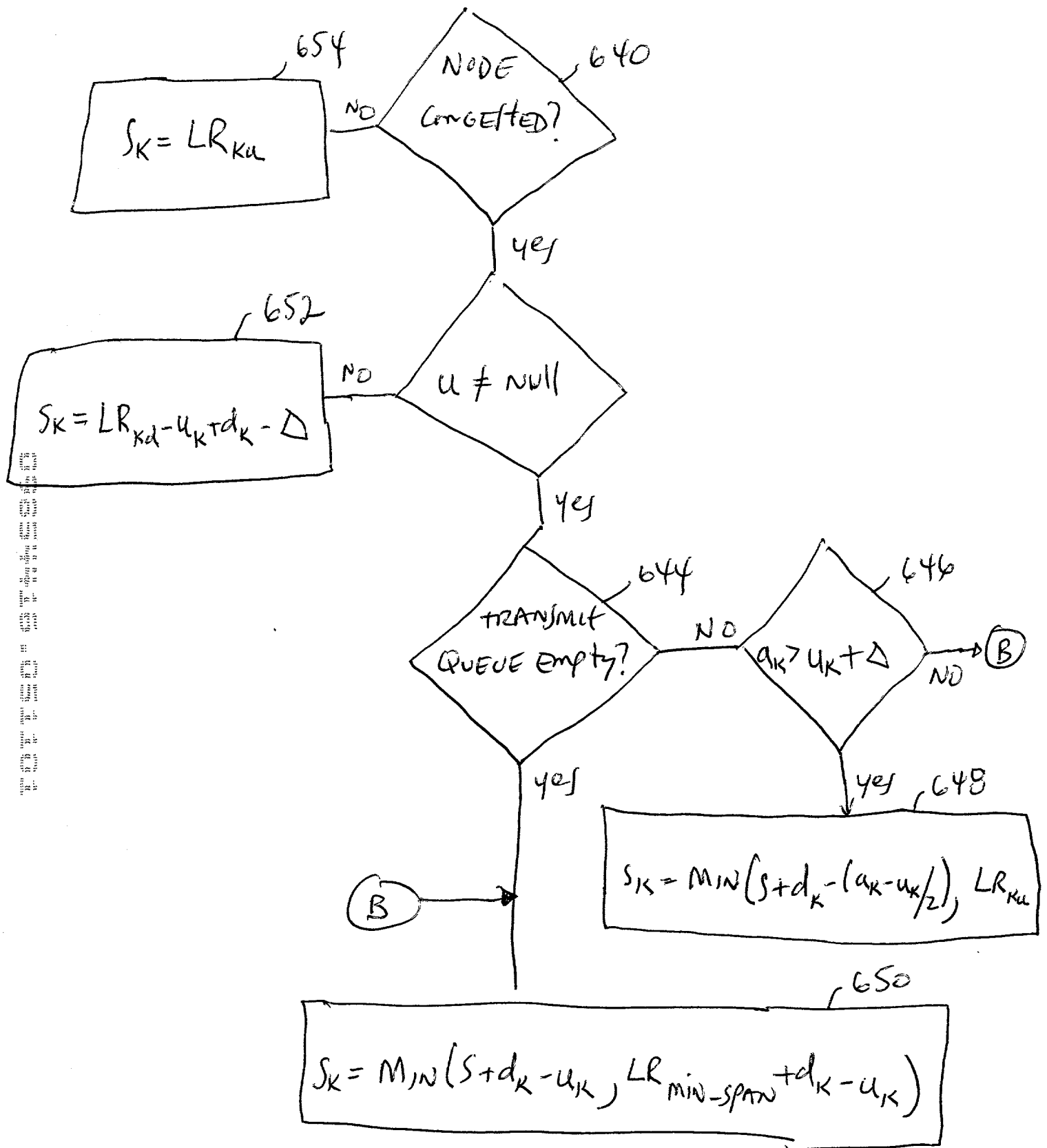


FIG. 6c